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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	10/068,087	
	Filing Date	February 6, 2002	
	First Named Inventor	Feniosky Pena-Mora	
	Group Art Unit	2163	
	Examiner Name	Not Yet Assigned	
Total Number of Pages in This Submission	17	Attorney Docket Number	MIT-087PUS

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Kermit Robinson Reg. No. 48,734 Daly, Crowley & Mofford, LLP
Signature	<i>Kermit Robinson</i>
Date	March 26, 2002

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Feniosky Peña-Mora, et al.

Appl. No. : 10/068,087

Filed : February 6, 2002

Entitled : RELIABILITY BUFFERING TECHNIQUE
APPLIED TO A PROJECT PLANNING
MODEL

DOCKET : MIT-087PUS

Group Art Unit: 2163

Examiner: Not Yet Assigned

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By Kermit Robinson
Kermit Robinson
Reg. No. 48,734
Attorney for Applicant(s)

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

LETTER TO CHIEF DRAFTSMAN

Upon approval of the Examiner in charge of the above-identified application, please substitute the enclosed drawing sheets containing formal versions of Figures 1, 1A, 2, 2A, 3, 4, 5, 5A, 6, 6A, 7, 7A, 7B and 7C (14 sheets) for the corresponding drawing sheets currently in the application.

Application No.: 10/068,087
Filed On: February 6, 2002
Atty. Doc. No.: MIT-087PUS

The Commissioner is hereby authorized to charge any other fees under 37 C.F.R. §1.16
and §1.17 that may be required, or credit any overpayment, to our Deposit Account No. 500845.

Respectfully submitted,

March 26, 2002
Date

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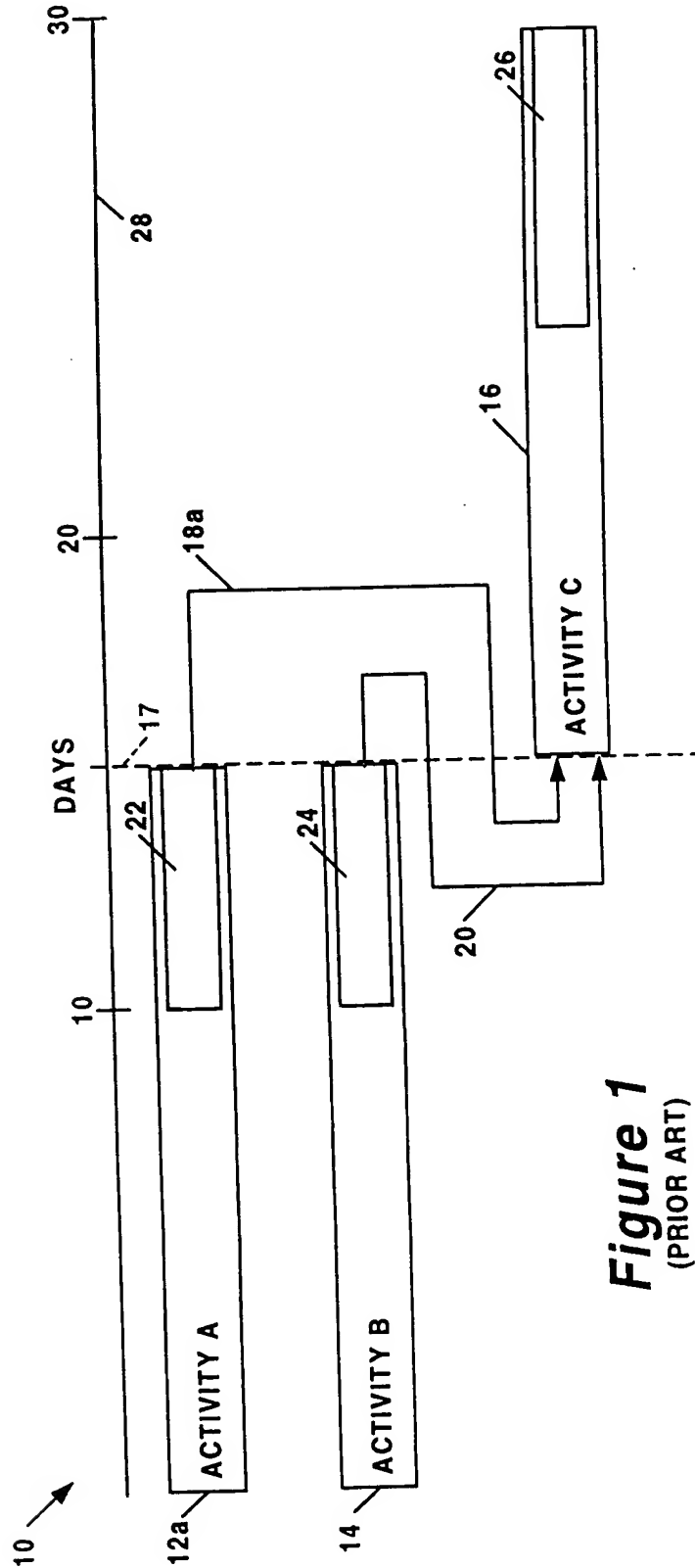


Figure 1
(PRIOR ART)

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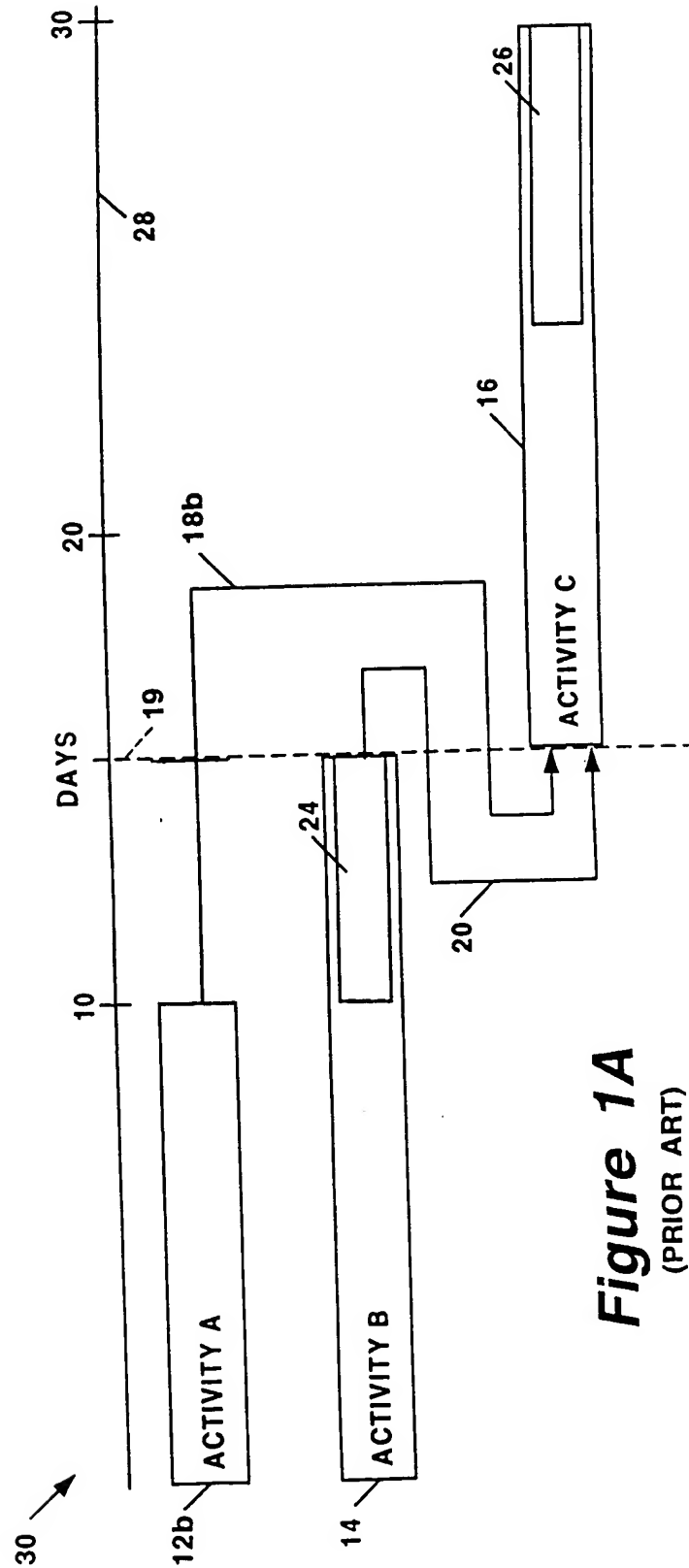


Figure 1A
(PRIOR ART)

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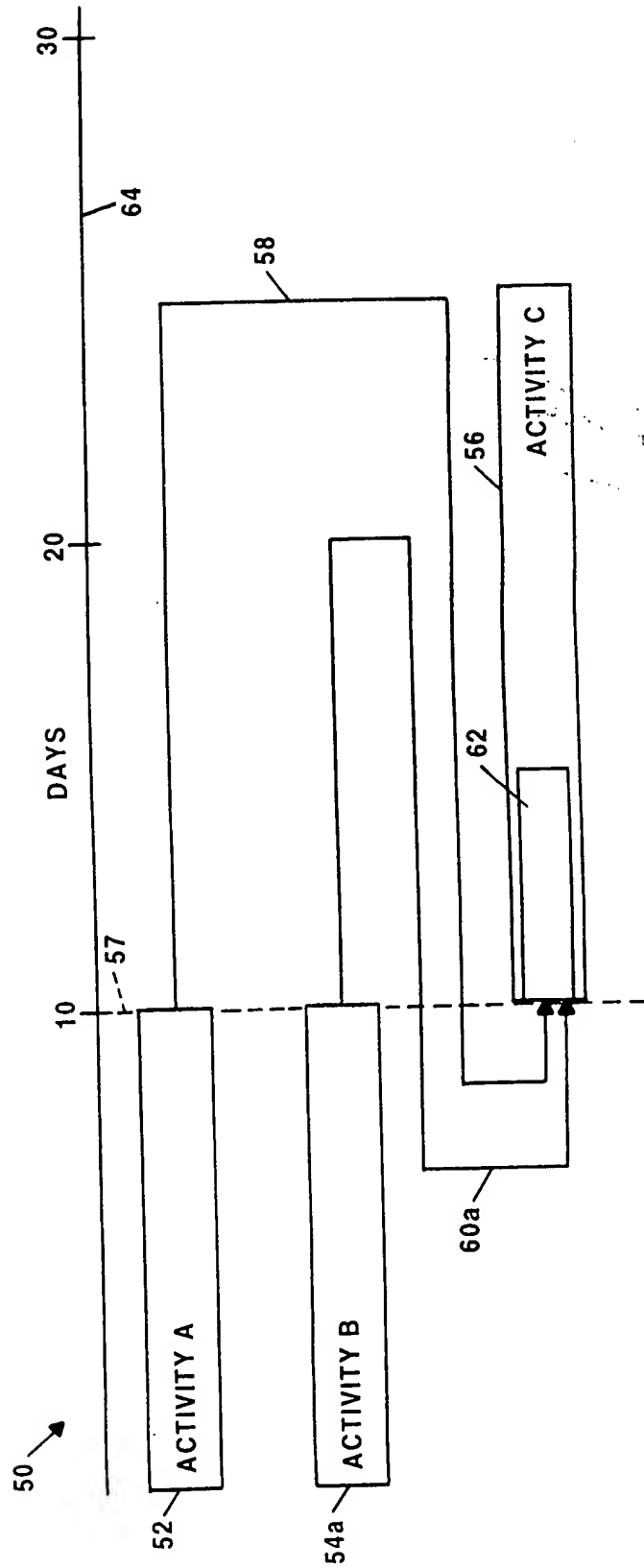


Figure 2

4/14

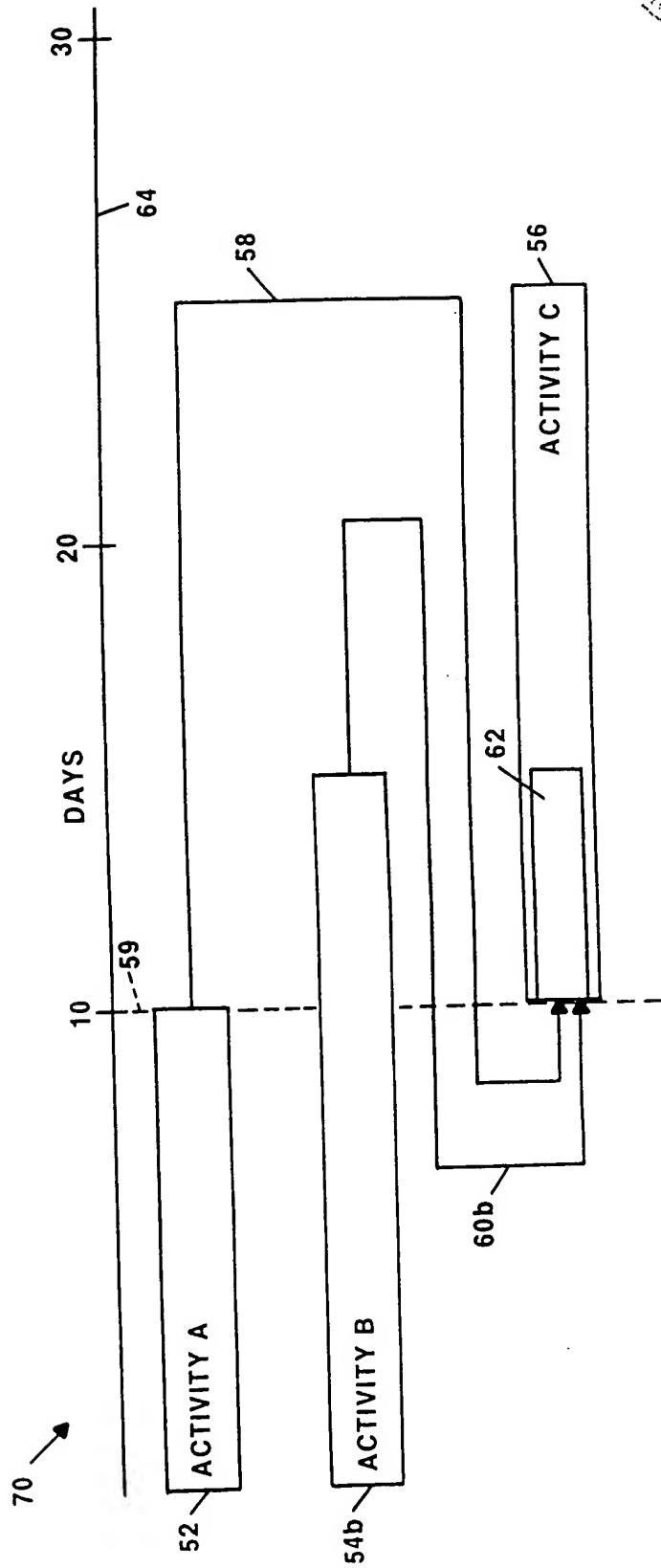


Figure 2A

5/14

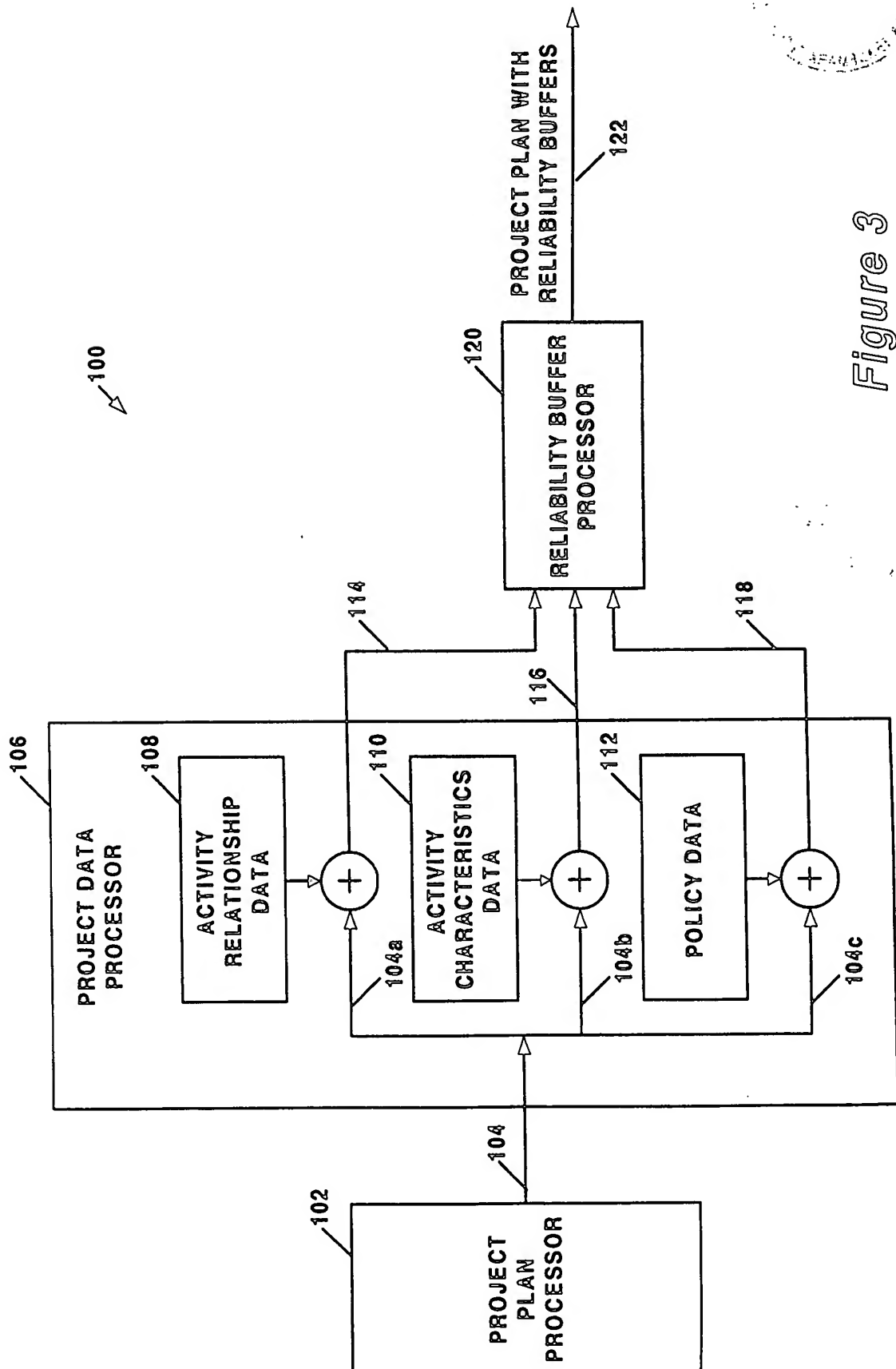


Figure 3

6/14

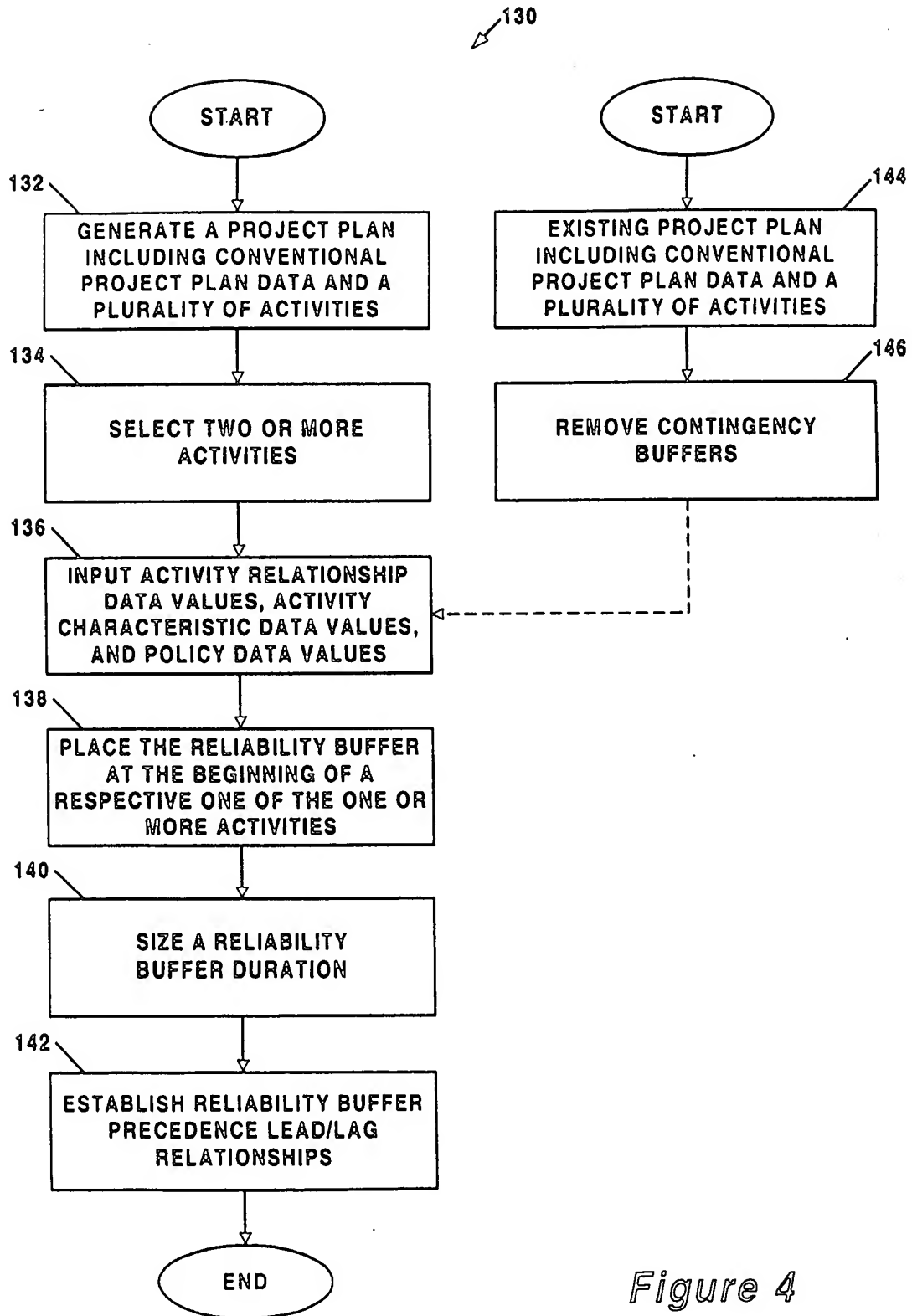


Figure 4

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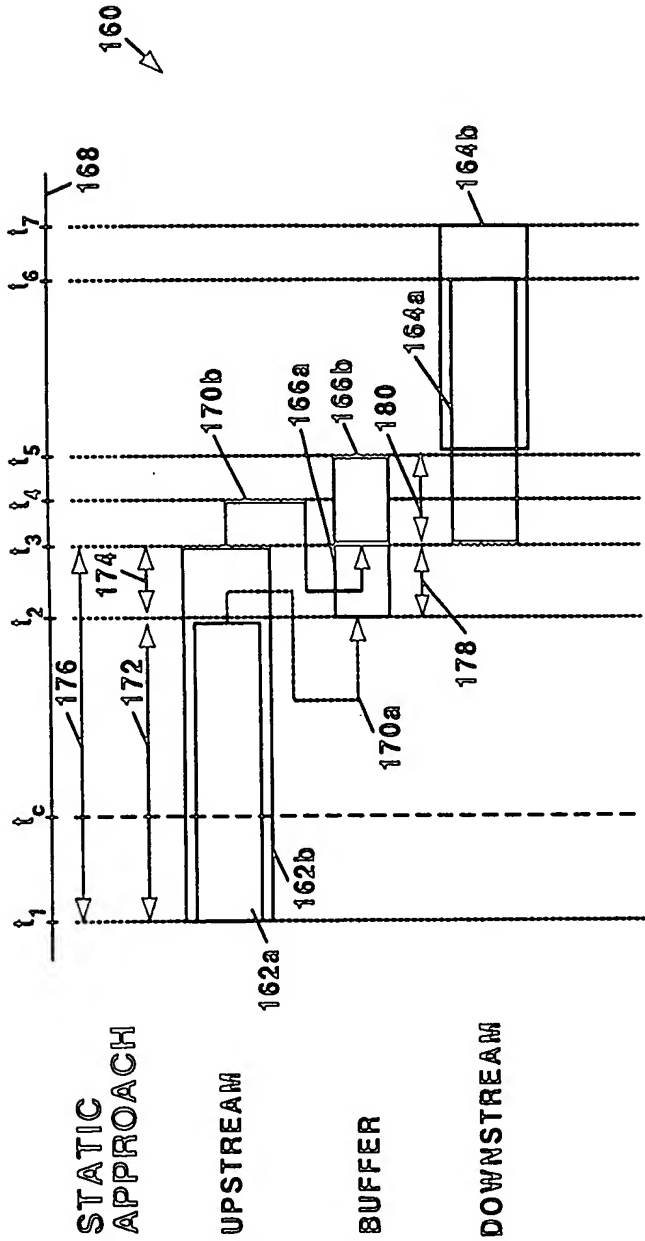


Figure 5

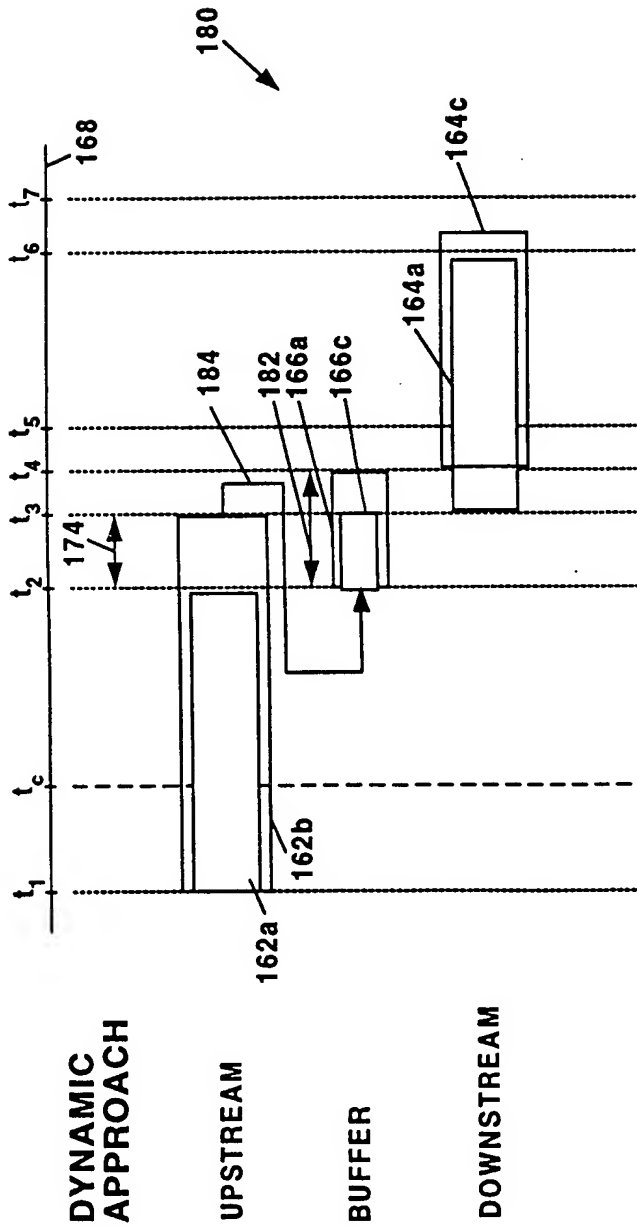


Figure 5A

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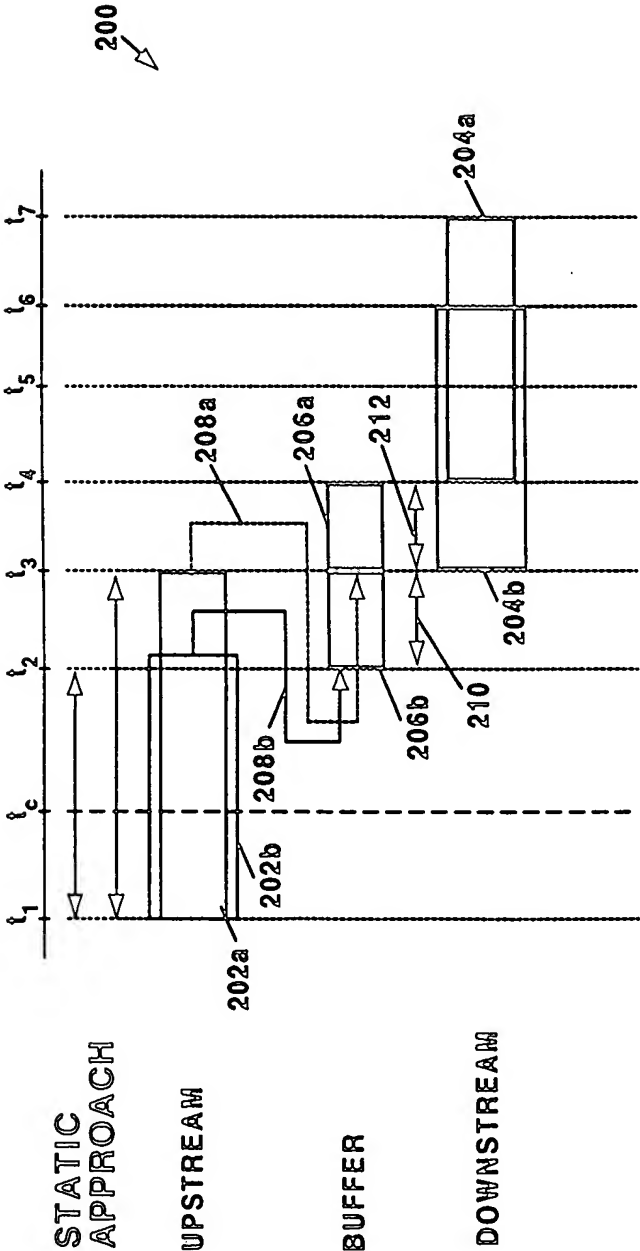


Figure 6

10/14

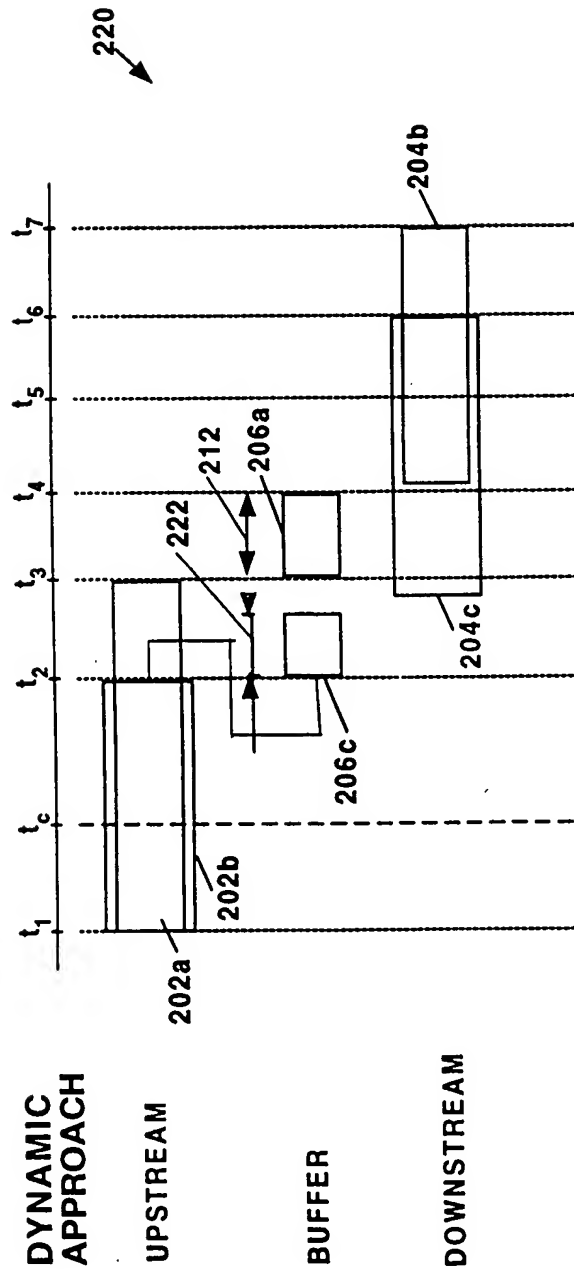


Figure 6A

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De- scription	With Leads			With Lags		
	Conditions / Precedence Relationships			Conditions / Precedence Relationships		
239a Initial	FS 0	$D_i = D_f$ 	$D_i = D_f$ 	$D_i = D_f$ 	FS (LI)	
239b Up- dated		$D_i > D_f$ 	$D_i > D_f$ 	$D_i > D_f$ 		
239c		$D_i < D_f$ 	$D_i < D_f$ 	$D_i < D_f$ 		

Figure 7

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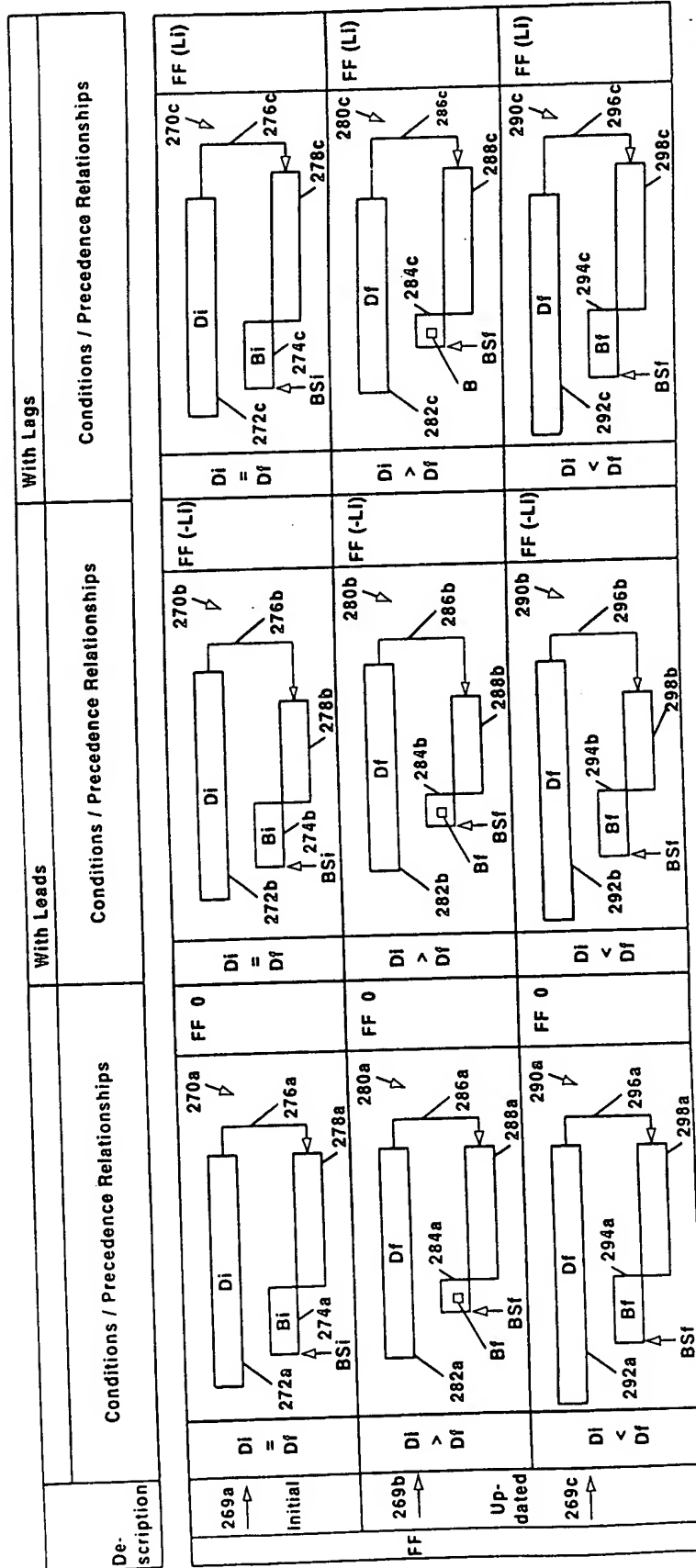


Figure 7A

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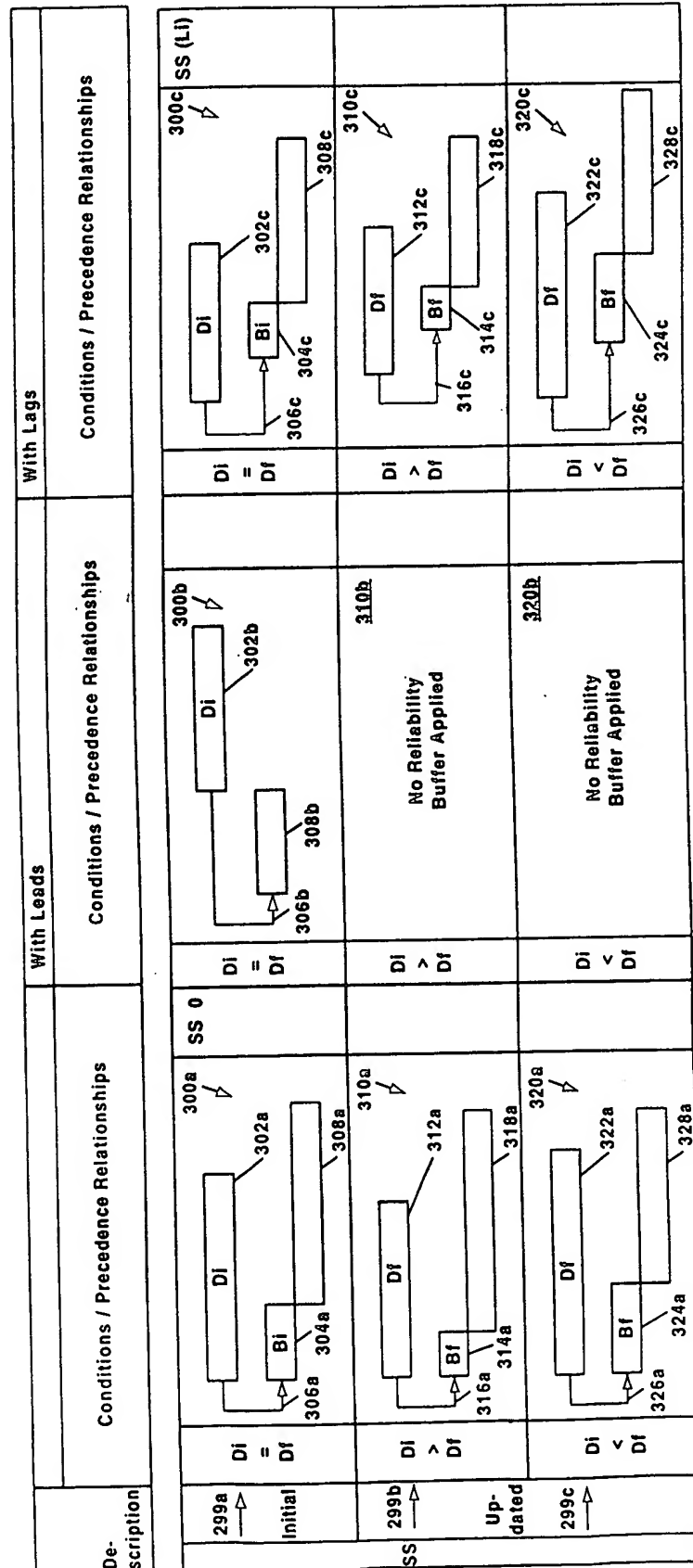


Figure 7B

14/14

De- scription	With Leads			With Lags		
	Conditions / Precedence Relationships			Conditions / Precedence Relationships		
329a ↑ Initial	330a ↑ 336a DI 332a 338a			330b ↑ 336b DI 332b 338b		332c ↑ 336c DI 334c BI 338c BSi
329b ↑ Up- dated	340a ↑ No Reliability Buffer Applied			340b ↑ No Reliability Buffer Applied		342c ↑ 344c DI 346c BI 348c BSi
329c ↑	350a ↑ No Reliability Buffer Applied			350b ↑ No Reliability Buffer Applied		350c ↑ 352c DI 354c BI 356c BSi
						350c ↑ 352c DI 354c BI 356c BSi

Figure 7C